



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 8228

Bib Data Sheet

SERIAL NUMBER 10/609,359	FILING DATE 06/27/2003 RULE	CLASS 365	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. SC12996TC
-----------------------------	---------------------------------------	--------------	------------------------	-------------------------------------

APPLICANTS

Gowrishankar L. Chindalore, Austin, TX;

Rajesh A. Rao, Austin, TX;

Jane A. Yater, Austin, TX;

** CONTINUING DATA

no 09/09/04 TNT

** FOREIGN APPLICATIONS

no 09/09/04 TNT

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 09/24/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>TNT</i> <i>Examining's Signature</i> <i>TNT</i> <i>Initials</i>	STATE OR COUNTRY TX	SHEETS DRAWING 5	TOTAL CLAIMS 19	INDEPENDENT CLAIMS 3
--	---	---------------------------	------------------------	-----------------------	----------------------------

ADDRESS

23125

FREESCALE SEMICONDUCTOR, INC.

LAW DEPARTMENT

7700 WEST PARMER LANE MD:TX32/PL02

AUSTIN , TX

78729

TITLE

Variable gate bias for a reference transistor in a non-volatile memory

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-----------------------------------	---	---